

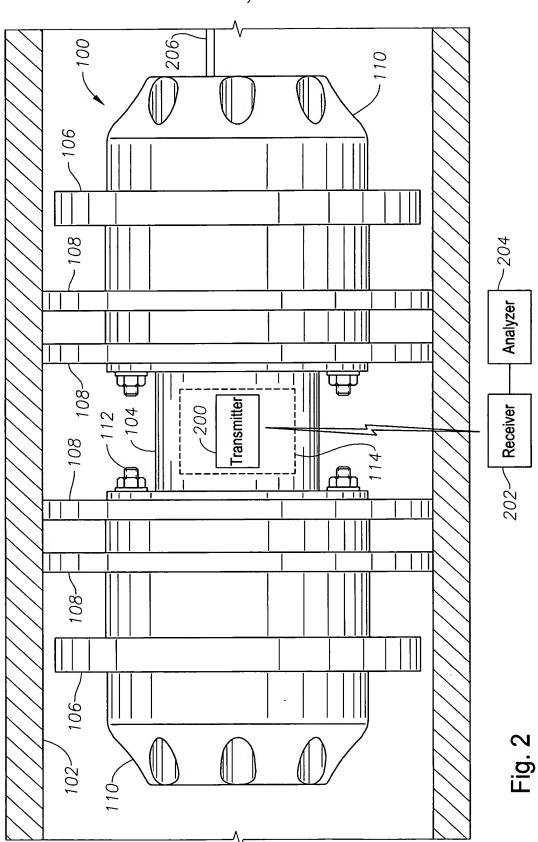
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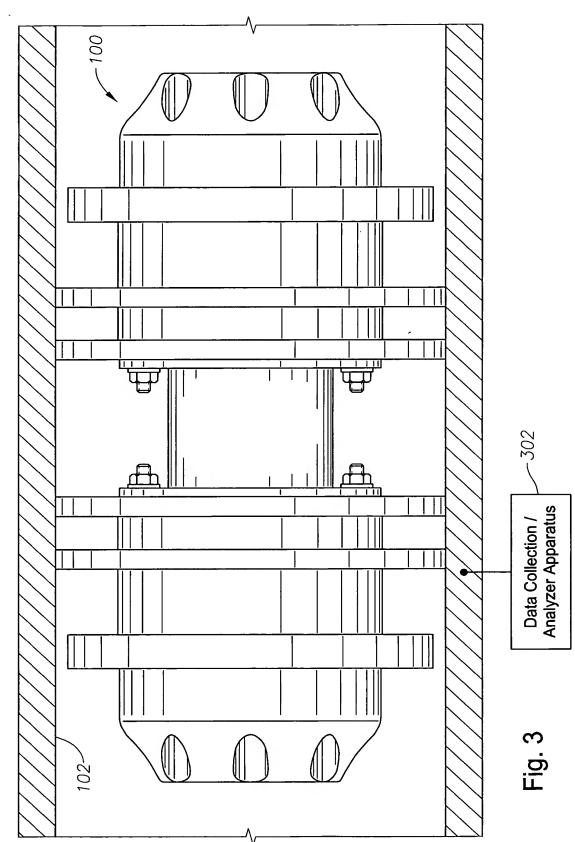
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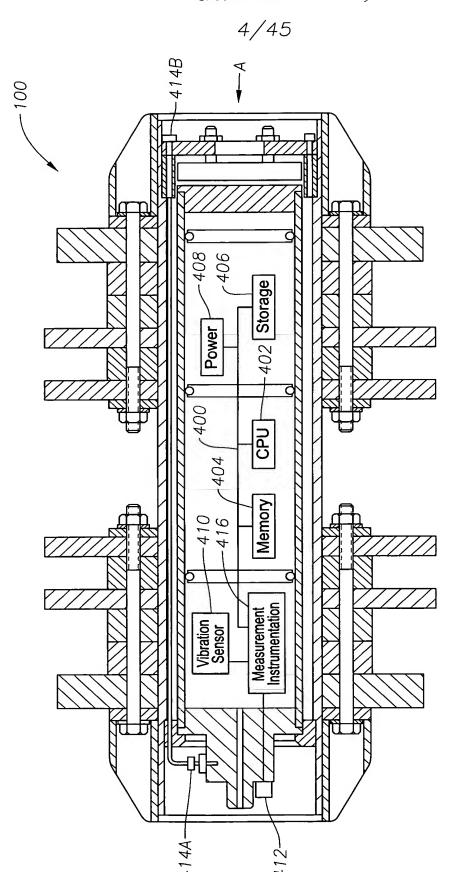
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F1g. 4

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5/45 Storage Power CPU Memory Measurement Instrumentation

FIG. 5

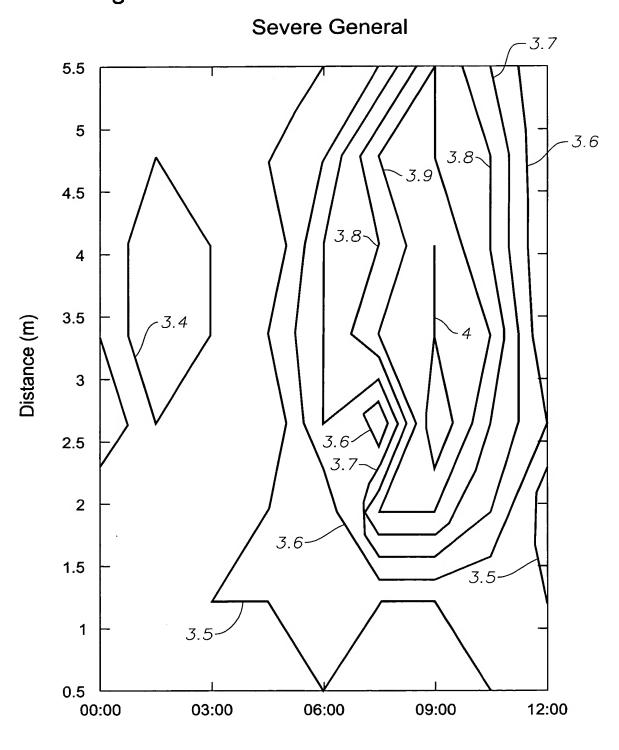
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Fig. 6A

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O'Clock Position



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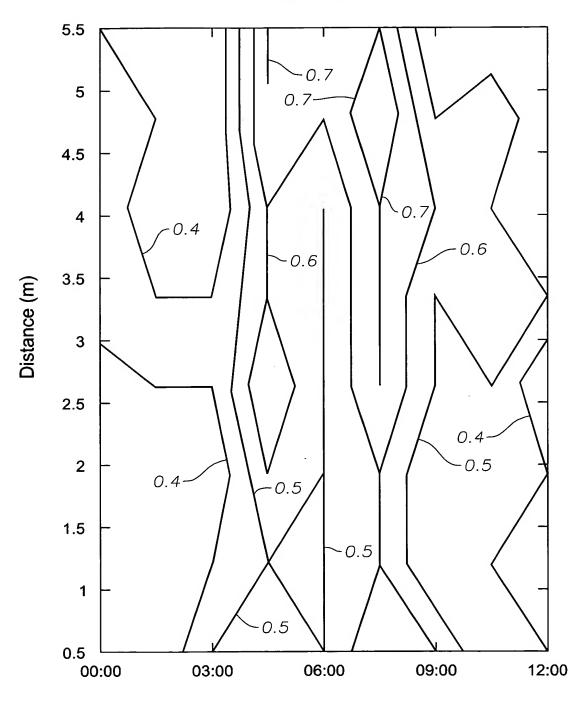
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Fig. 6B

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Slight General



O'Clock Position

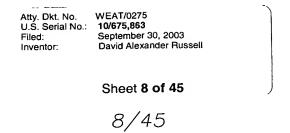
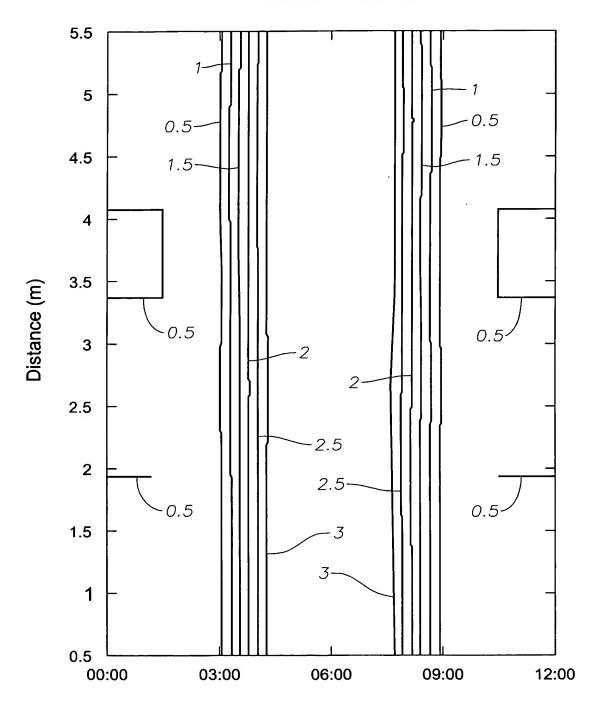


Fig. 6C

Severe Channel



O'Clock Position

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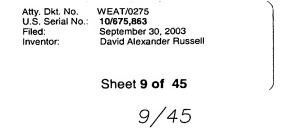
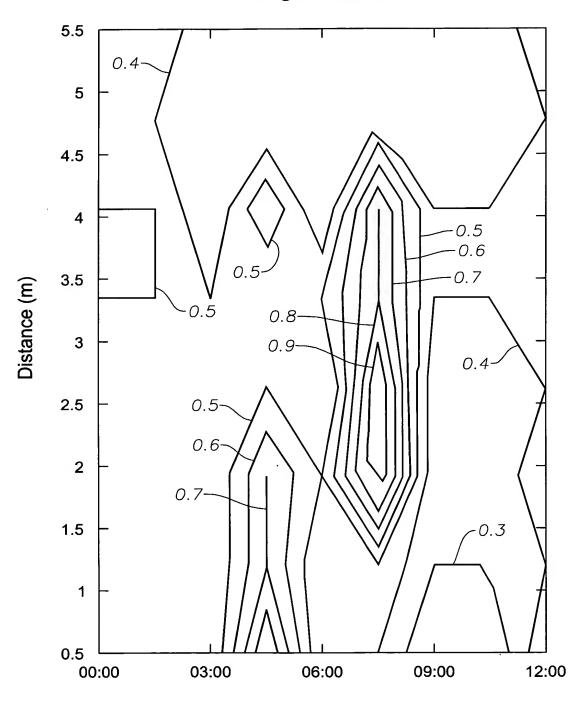


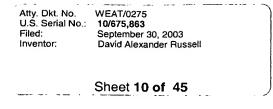
Fig. 6D

Slight Channel

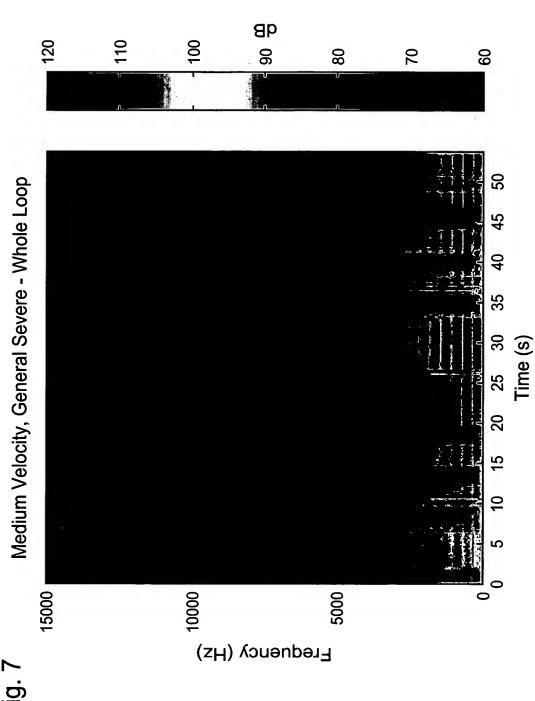


O'Clock Position

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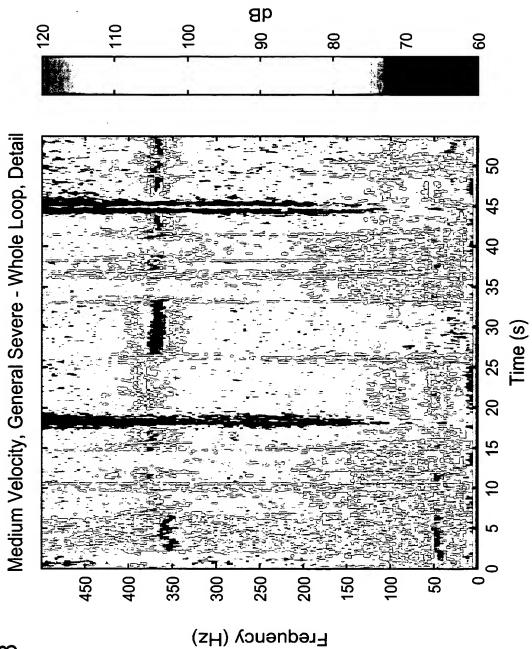


Fig. 8

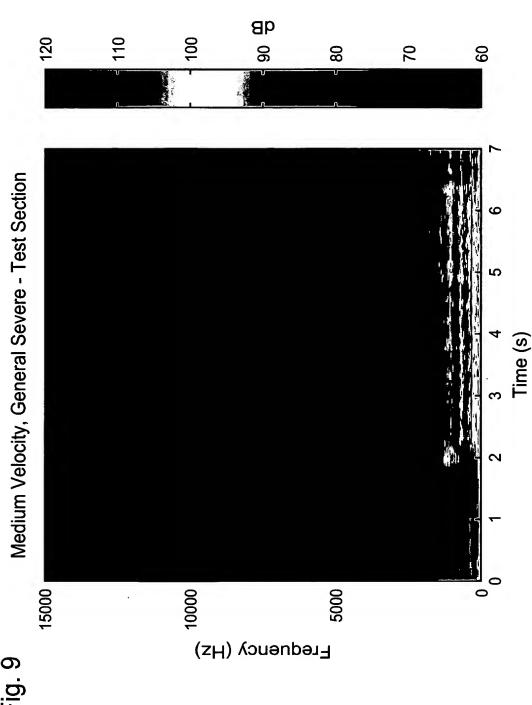
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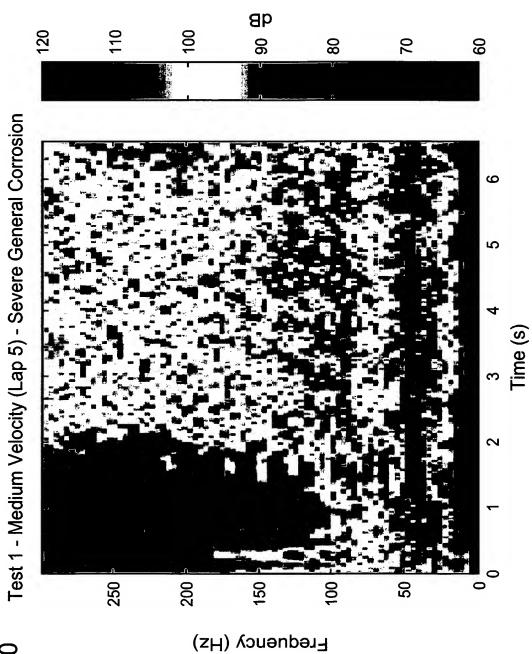


Fig. 10

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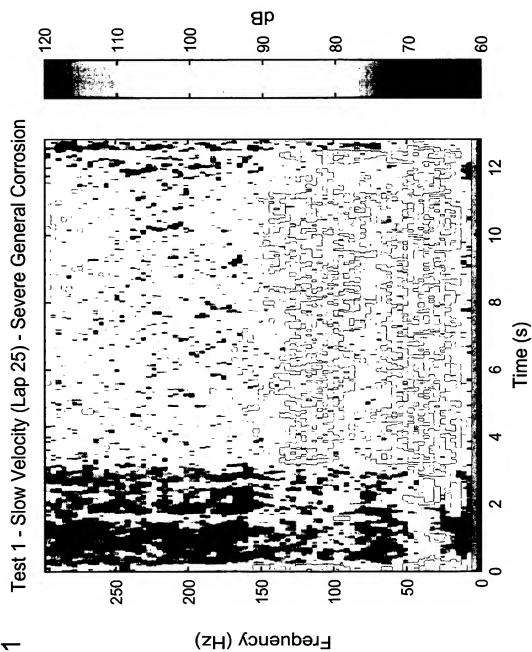


Fig. 11

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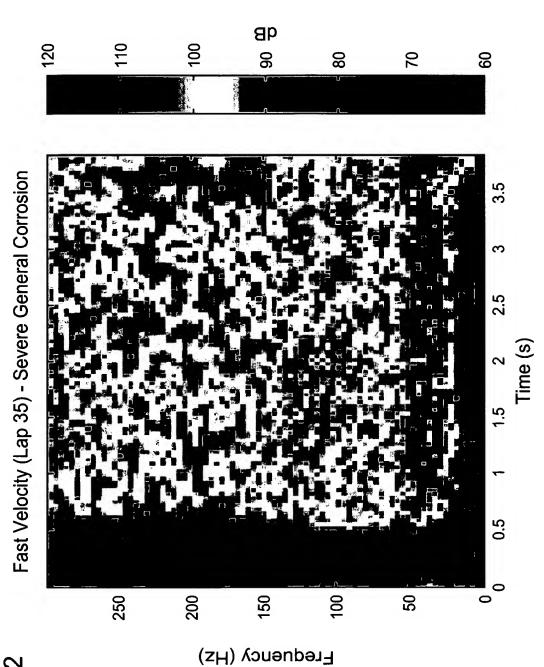


Fig. 12

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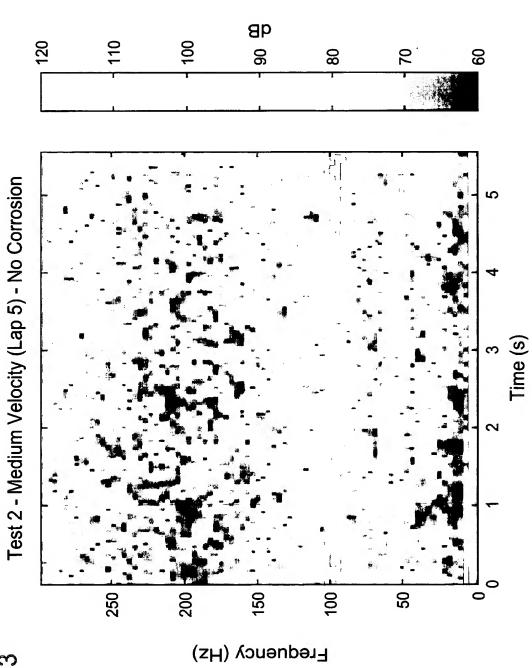


Fig. 13

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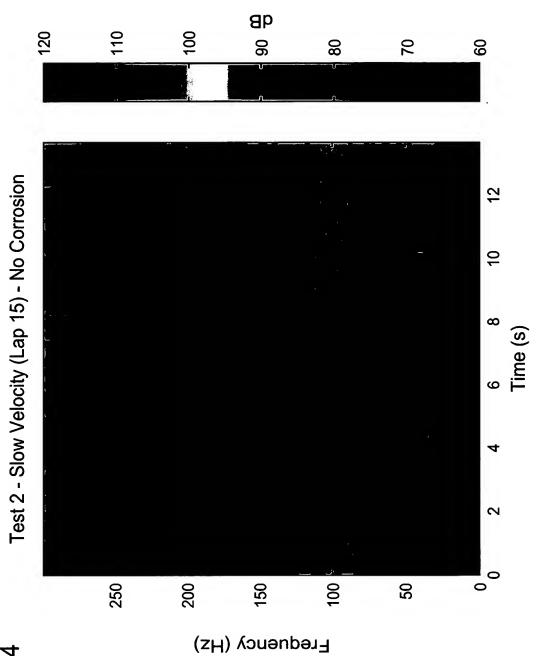


Fig. 14

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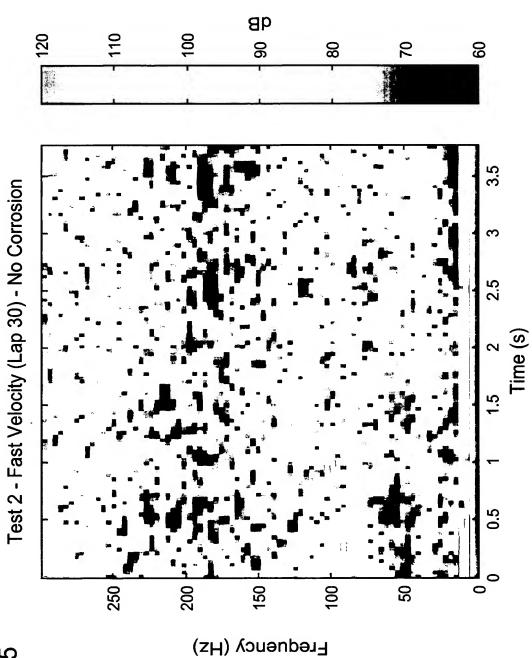
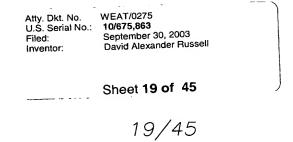


Fig. 15

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ЯÞ 120 8 80 70 9 Test 2 - Medium Velocity (Lap 7) - Anomalous - No Corrosion 200 20 250 150 Frequency (Hz)

Fig. 16

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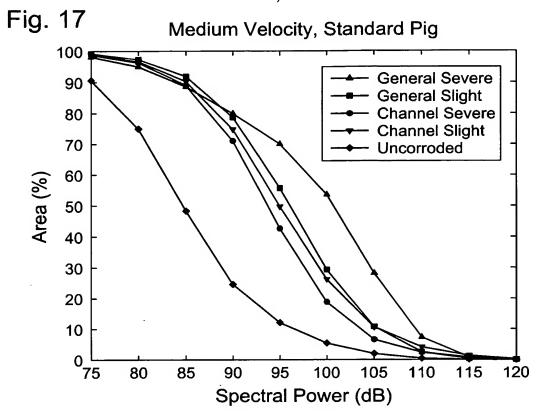
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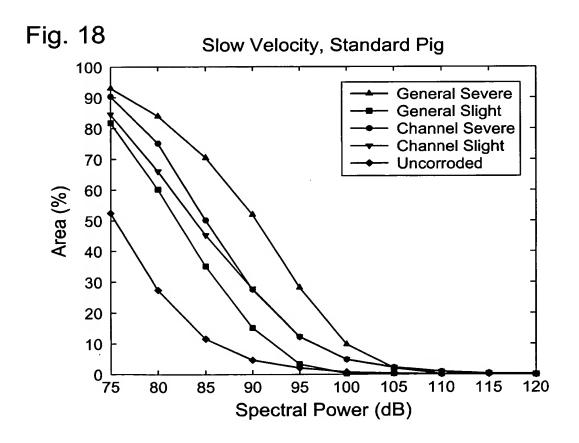
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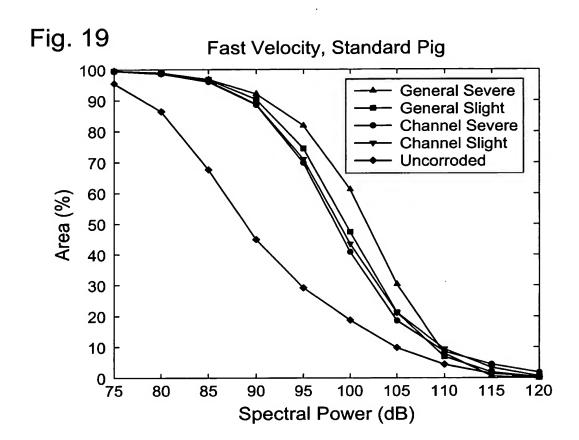
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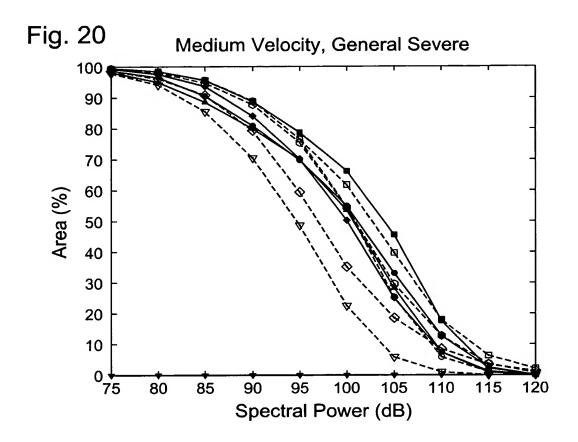
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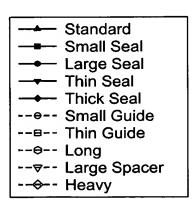
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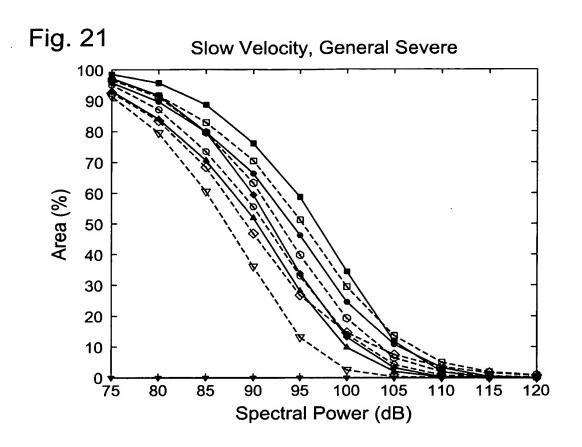


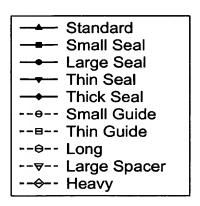
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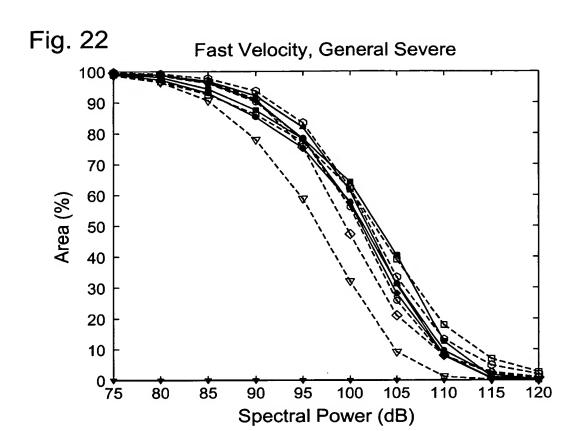


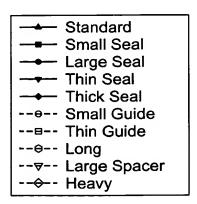
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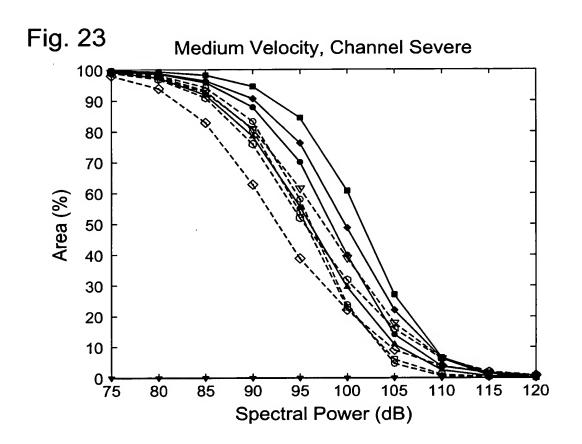


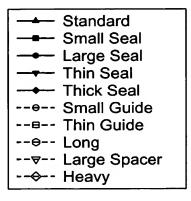
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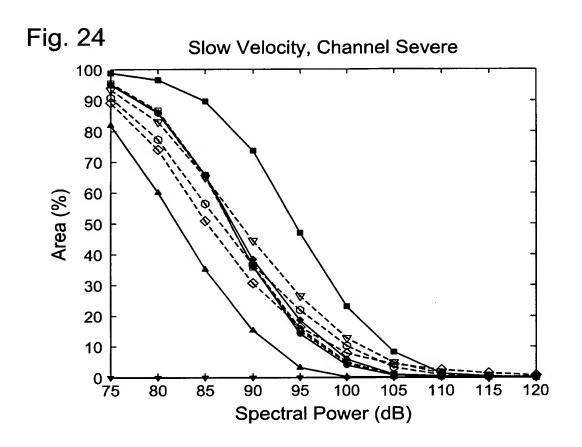


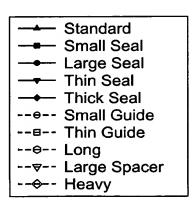
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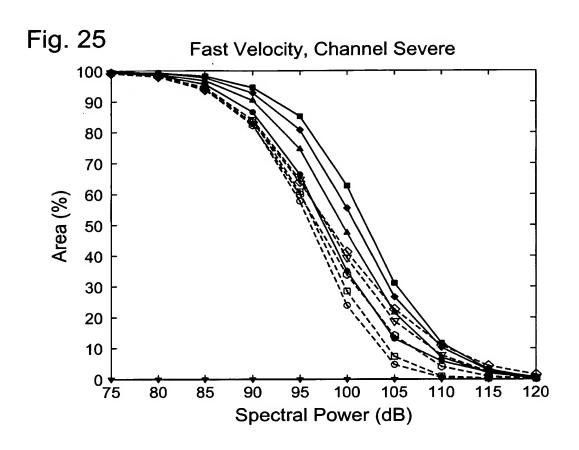
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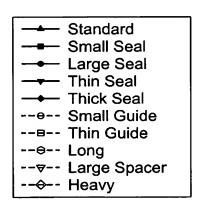
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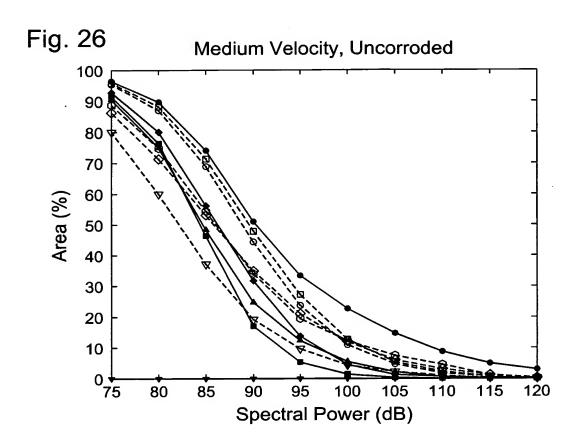


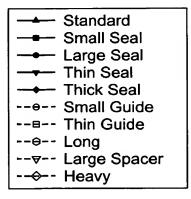
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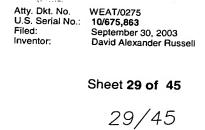
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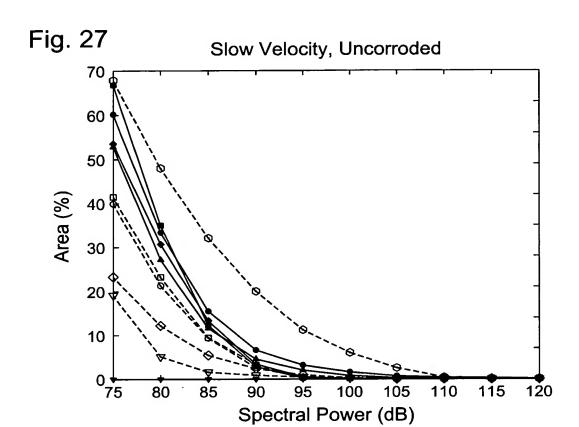


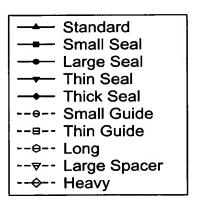


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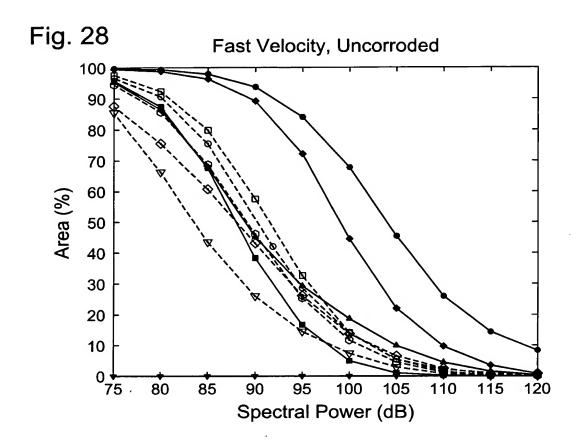


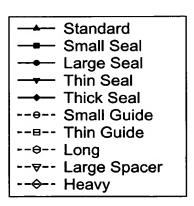
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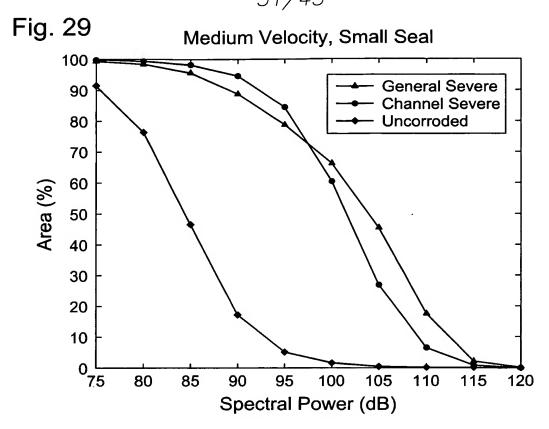
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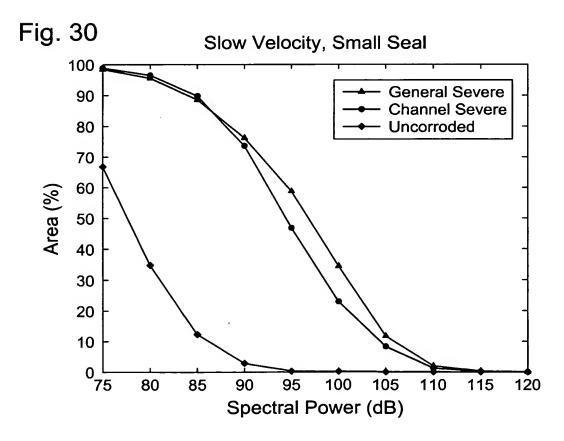
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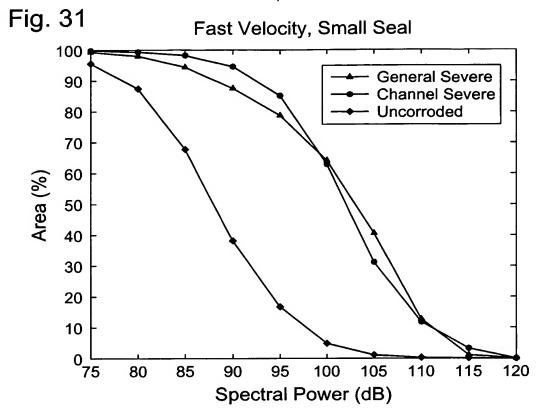
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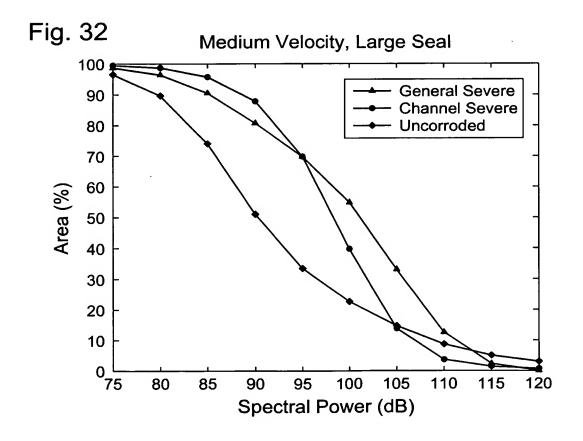
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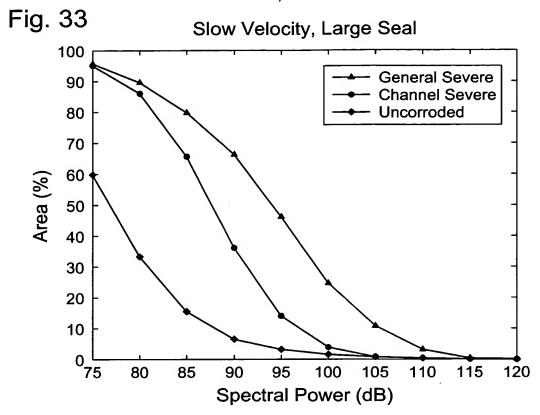
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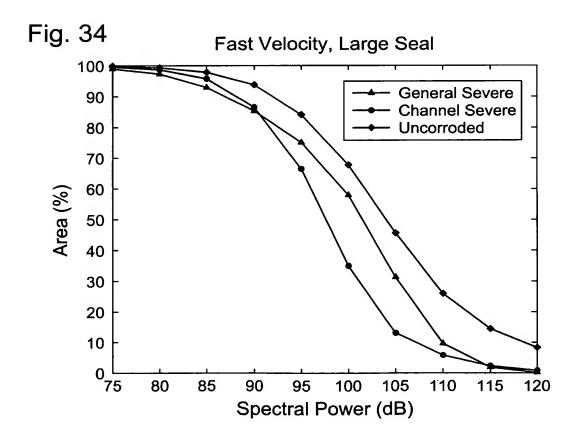
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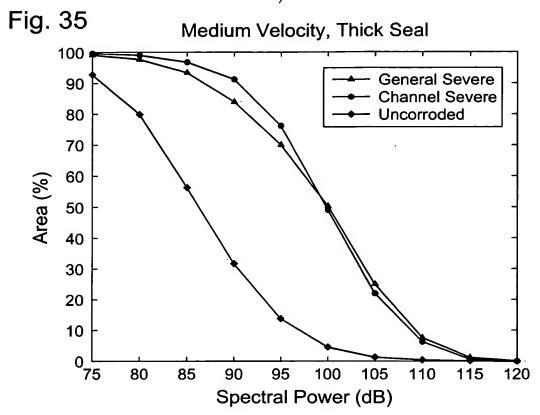


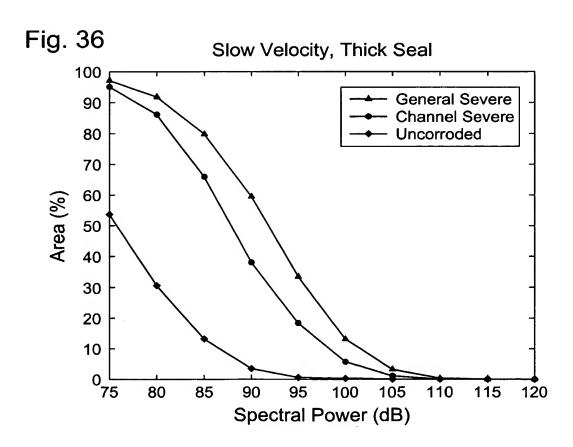
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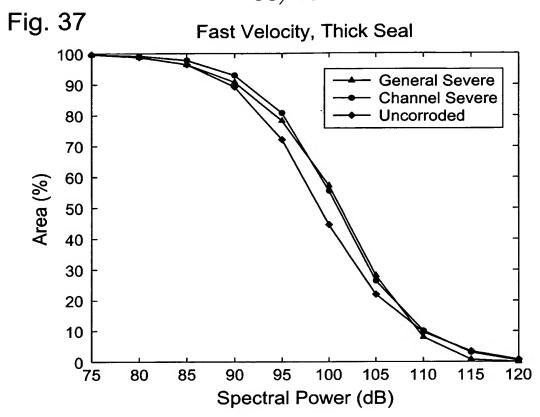


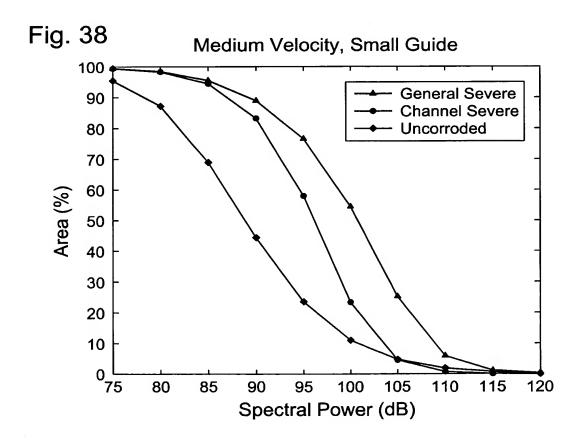
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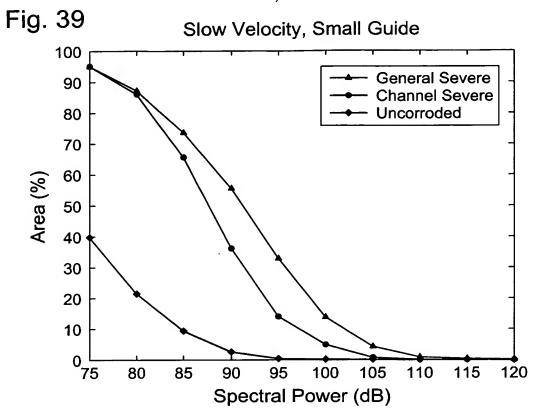
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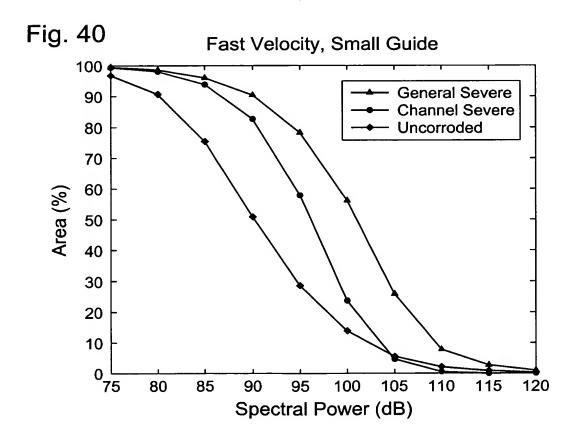
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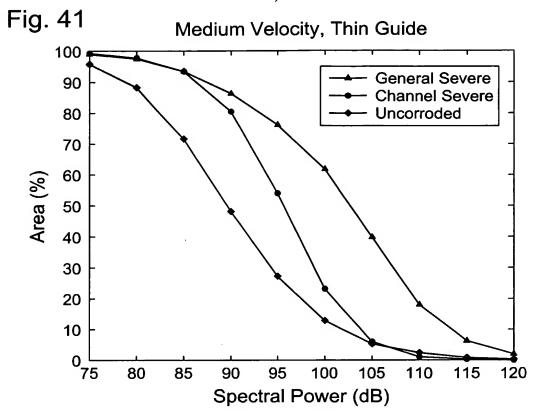
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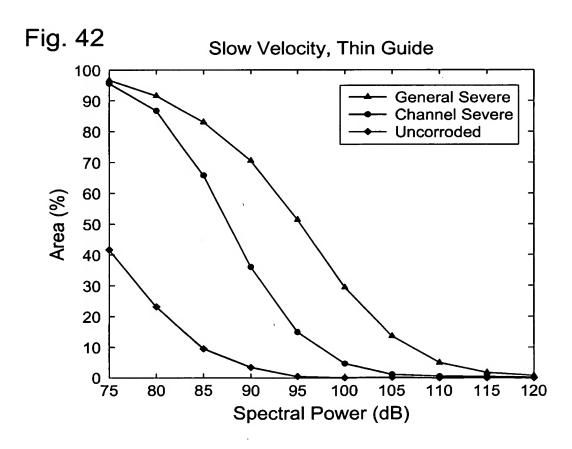
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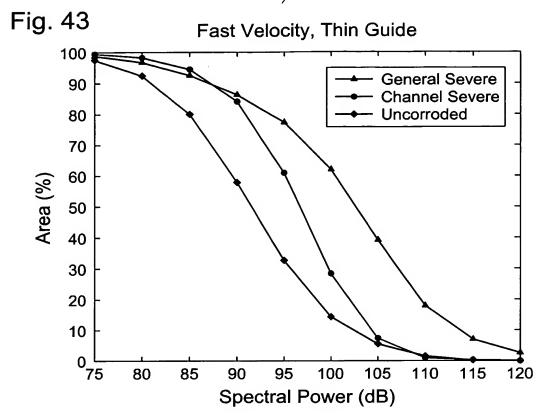
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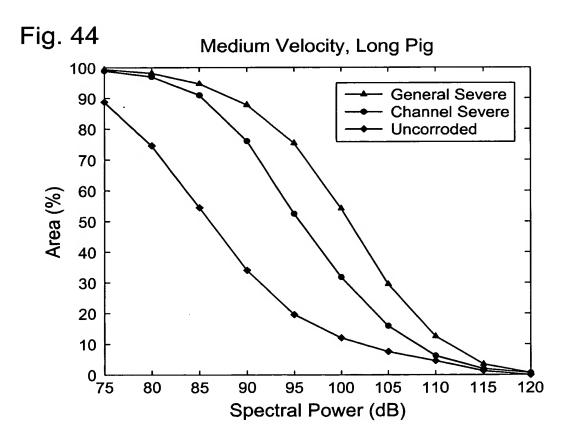
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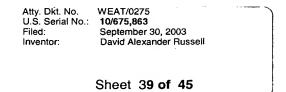
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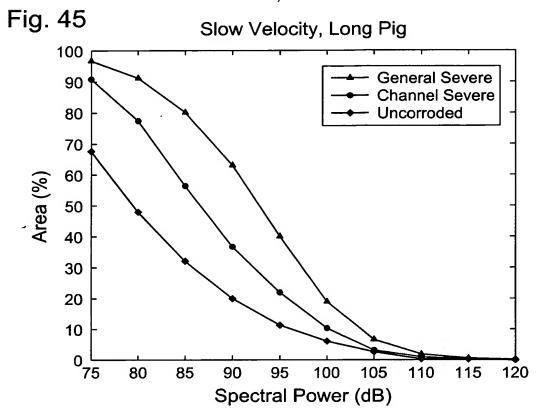


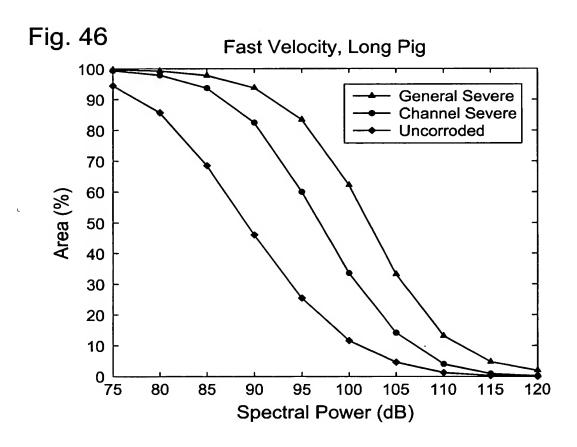




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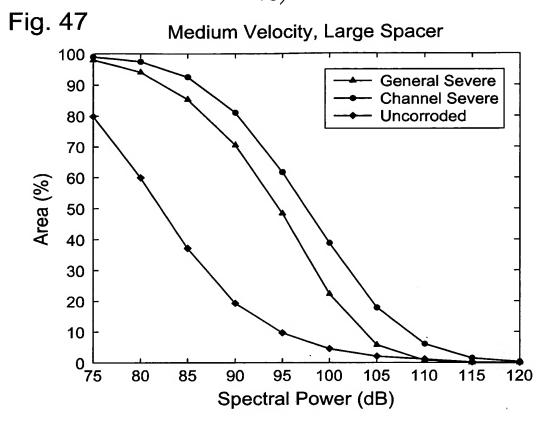


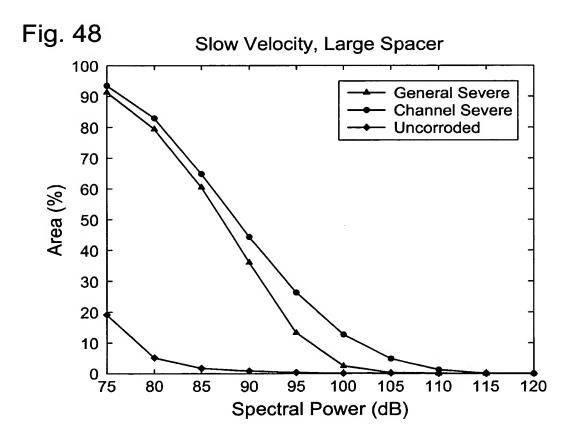
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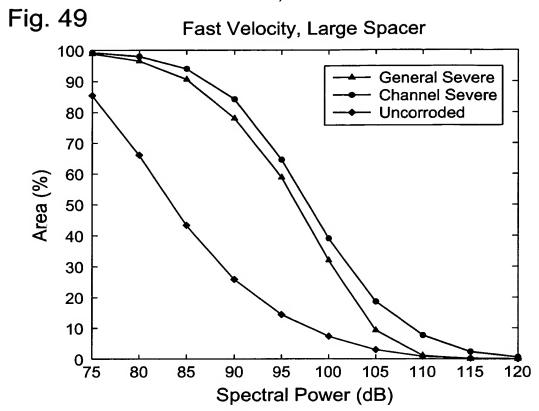
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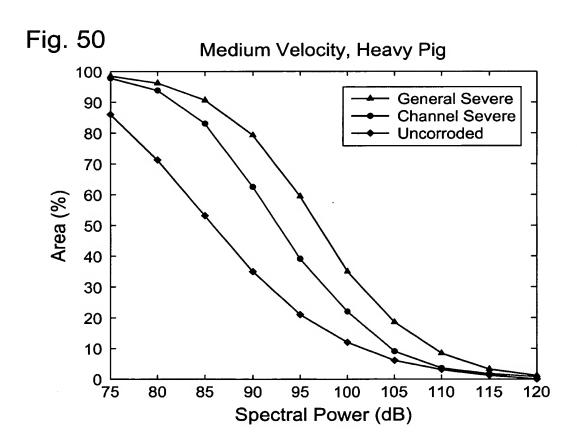
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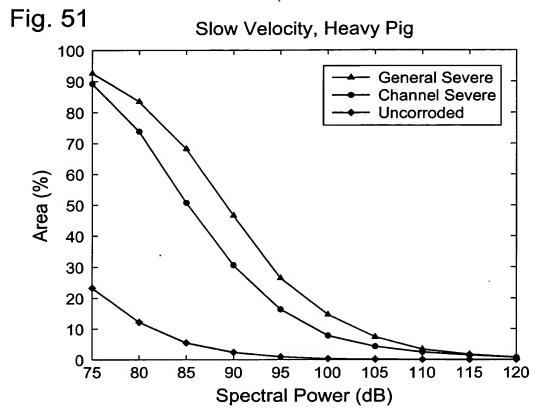
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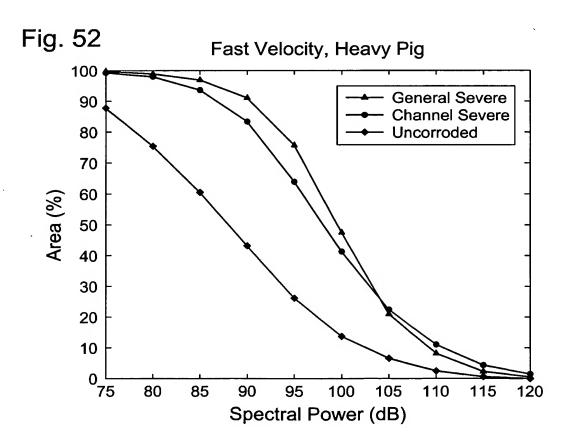
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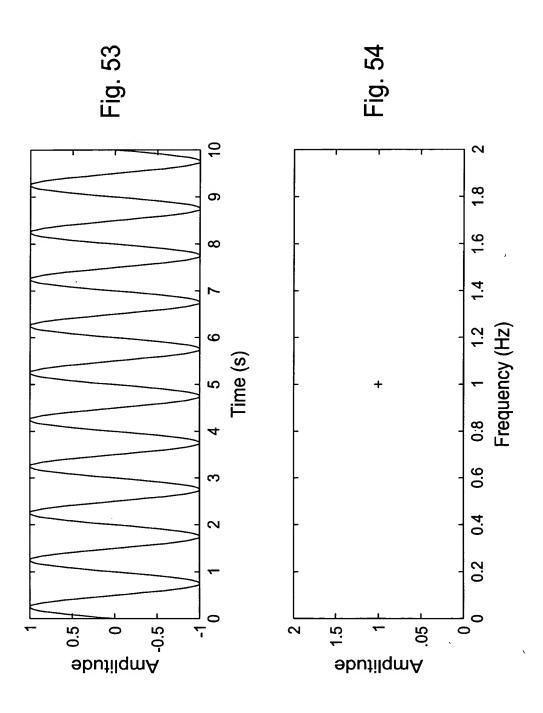




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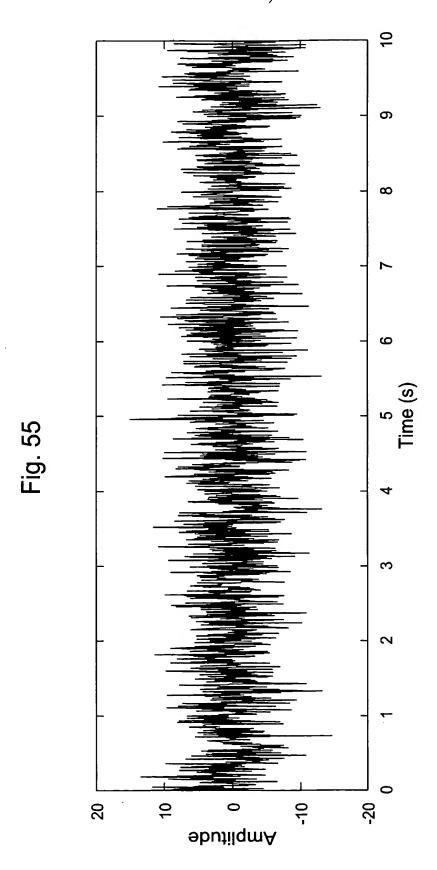


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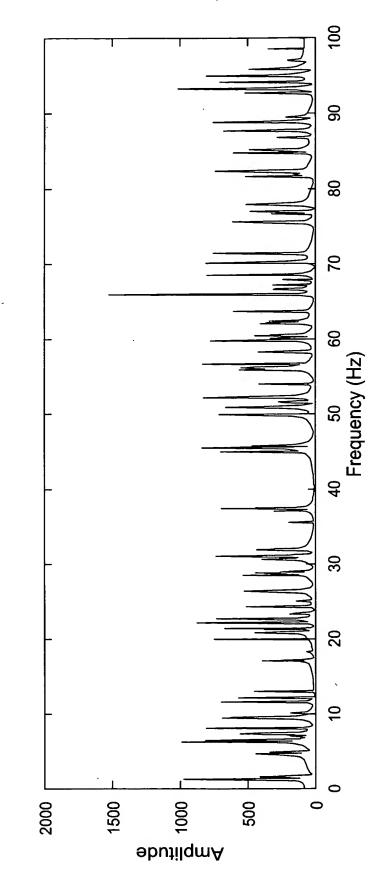
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